3/B 8/10/99

PATENT Attorney Docket No. 1333.0116-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Continuation Application of:)
Paul E. FLEISCHER ET AL.)
Parent Reissue Application Serial No.: 08/555,196)) Group Art Unit: 2734
Filed: Concurrently) Examiner: D. Vo
For: SYNCHRONOUS RESIDUAL TIME STAMP FOR TIMING RECOVERY IN A BROADBAND NETWORK))))
Assistant Commissioner for Patents Washington, D.C. 20231	
Sir:	
PRELIMINARY AMENDMENT	
Prior to the examination of the above continuation application, please amend this	
application as follows:	
IN THE CLAIMS: Please cancel claims 1-10 and 12-32. Please add new claims 33-36 as follows:	

LAW OFFICES
FINNEGAN, HENDERSON,
FARABOW, CARRETT,
& DUNNER, L. P.
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WASHINGTON, D. 9. 200005
2021 408 4000

- - 33. A method for representing a timing clock of a service input signal at a source node of a packet-based communications network including a network clock, the method comprising the steps of: